

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	53393	(account or accounting) near5 (memory or unit or module or card or device or vault or smartcard or smd or ped or pmd or smu or peu or pmu or register or psa or psd)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:54
2	BRS	L2	3367	1 near10 (encrypt or encrypted or encrypting or encryption or code or coded or coding or encode or encoded or encoded or encipher or enciphered or enciphering or cipher or ciphering or ciphered)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:54
3	BRS	L3	176262	(plural or plurality or multiple or multi or change or changed or changing or switch or switched or switching or replace or replaced or replacing) near5 (seed or random or key or prime)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:54
4	BRS	L4	92203	(3 or seed or random or key or prime) near5 (encrypt or encrypted or encrypting or encryption or code or coded or coding or encode or encoded or encoded or encipher or enciphered or enciphering or cipher or ciphering or ciphered)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:55
5	BRS	L5	1109	2 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:55

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	66506	(print or printed or printing or stamp or stamped or stamping or mark or marked or marking or imprint or imprinted or imprinting) near5 (encrypt or encrypted or encrypting or encryption or code or coded or coding or encode or encoded or encoded or encipher or enciphered or enciphering or cipher or ciphering or ciphered)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:56
7	BRS	L7	11454	(postage or shipping or frank or franking or franked or mail or mailing) near5 (indicia or indicium or mark or image or impression or image or marking)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:56
8	BRS	L8	457	6 near5 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:56
9	BRS	L9	28	1 and 5 and 8 <i>Scanned Ti, Ab, Huic all</i>	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 08:59

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	437226	(remote or remotely or center or central or centrally) near3 (install or installed or installing or installation or load or loaded or loading or initial or initialized or initializing or inititalization or control or controlled or controlling)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:32
2	BRS	L2	269	1 near5 (postage or franking or mailing or shipping)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:33
3	BRS	L3	176262	(plural or plurality or multiple or multi or change or changed or changing or switch or switched or switching or replace or replaced or replacing) near5 (seed or random or key or prime)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:33
4	BRS	L4	92203	(3 or seed or random or key or prime) near5 (encrypt or encrypted or encrypting or encryption or code or coded or coding or encode or encoded or encoded or encipher or enciphered or enciphering or cipher or ciphering or ciphered)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:34

	Type	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L5	12	2 same 4 <i>Scanned Ti, Ab, Kwic all</i>	US - PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:34
6	BRS	L6	22635	fpga or (field near2 program\$5 near2 gate near2 array)	US - PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:39
7	BRS	L7	0	6 same 2	US - PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 13:20
8	BRS	L8	0	6 and 2	US - PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 12:40
9	BRS	L9	34	6 near10 1 <i>Scanned Ti, Ab, Kwic all</i>	US - PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 13:22

	Type	L #	Hits	Search Text	DBs	Time Stamp
10	BRS	L10	62	<pre>((@pd<="19710101" not @pd<="19470101") and (713/1 or 713/2 or 713/600).ccls.)</pre> <p><i>Scanned Ti cell</i></p>	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/16 13:50

US-PAT-NO: 5794033

DOCUMENT-IDENTIFIER: US 5794033 A

TITLE: Method and system for in-site and on-line reprogramming of hardware logics with remote loading in a network device

DATE-ISSUED: August 11, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Aldebert; Jeane-Paul	Vence	N/A	N/A	FR
Basso; Claude	Nice	N/A	N/A	FR
Calvignac; Jean	La Gaude	N/A	N/A	FR
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Orsatti; Daniel	Cagnes Sur Mer	N/A	N/A	FR
Verplanken; Fabrice	La Gaude	N/A	N/A	FR
Zunino; Jean-Claude	Nice	N/A	N/A	FR

US-CL-CURRENT: 713/100, 326/39, 365/185.33, 365/189.08, 708/232, 710/1, 711/103, 712/248

ABSTRACT: The invention discloses a method and an apparatus for in-line and on-site updating of Field Programmable Gate Arrays with remote loaded configuration data files. Flash EEPROMs which are used because of their non-volatile memories and their high density, are storing more than one configuration data file. The memories are divided in more than one part, each part of the memory for storing one configuration data file. One part of the memory also contains a flag identifying the currently loaded configuration data file. The Flash EPROM's bits being set to one same binary value before any writing operation, including the update of the configuration data file containing the flag. The setting of the bits to said binary value always identifies a valid other configuration data file in order to insure a correct re-loading of the FPGAs in case of reception of an unexpected event leading to an initialization.

13 Claims, 11 Drawing figures

Exemplary Claim Number: 10

Number of Drawing Sheets: 11

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Abstract Text - ABTX (1): The invention discloses a method and an apparatus for in-line and on-site updating of Field Programmable Gate Arrays with remote loaded configuration data files. Flash EEPROMs which are used because of their non-volatile memories and their high density, are storing more than one configuration data file. The memories are divided in more than one part, each part of the memory for storing one configuration data file. One part of the memory also contains a flag identifying the currently loaded configuration data file. The Flash EPROM's bits being set to one same binary value before any writing operation, including the update of the configuration data file containing the flag. The setting of the bits to said binary value always identifies a valid other configuration data file in order to insure a correct re-loading of the FPGAs in case of reception of an unexpected event leading to an initialization.

Brief Summary Text - BSTX (17): Taking into account the knowledge of prior art, it is an object of the present invention to provide a mode of reprogramming of FPGAs with new configuration data in a network device such as a switching node controlled by a local or remote processor able to manage this update.

	Document ID	Issue Date	Inventor	Current OR	Current XRef	Pages
1	US 5794033 A	19980811	Aldebert; Jeane-Paul et al.	713/100	326/39; 365/185.33; 365/189.08; 708/232; 710/1; 711/103; 712/248	18

19 results

	Document ID	Issue Date	Inventor	Current OR Current XRef	Pages
1	EP 862145 A2	19980902	GILHAM, DENNIS THOMAS		9
2	US 6567794 B1	20030520	Cordery; Robert A. et al.	705/60 705/75	16
3	US 6308165 B1	20011023	Gilham; Dennis Thomas	705/62	8
4	US 6073125 A	20000606	Cordery; Robert A. et al.	705/1; 705/400; 705/401; 705/402; 705/403; 705/408; 705/51; 705/63; 705/64	8
5	US 5768132 A	19980616	Cordery; Robert A. et al.	705/410	18
6	US 5675650 A	19971007	Cordery; Robert A. et al.	380/29; 380/30; 380/51; 380/55; 705/401; 705/405	14
7	US 5606613 A	19970225	Lee; Young W. et al.	705/62	380/51 6
8	US 4900904 A	19900213	Wright; Christopher B. et al.	235/380; 235/441; 705/407; 705/410	23

19 results

	Document ID	Issue Date	Inventor	Current OR	Current XRef	Pages
9	US 4864618 A	19890905	Wright; Christopher B. et al.	380/51	340/5-8; 705/60; 705/62	23
10	US 4802218 A	19890131	Wright; Christopher B. et al.	705/60	235/375; 235/380; 235/487; 235/492; 380/51; 705/61; 705/67; 705/68; 713/166	27

L9 results